



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Rinco-Mora, et al.

Serial No.: 09/977,609

Filing Date: 10/15/01

Art Unit: 2816

Examiner: Nguyen, Minh

Docket No.: TI-33257

#8  
Hew/Dwgs  
J. Macmillan  
3/5/03

Title: A SIMPLE FLOATING-GATE MOS THRESHOLD VOLTAGE EXTRACTION  
CIRCUIT & AN ACCURATE LINEAR CAPACITOR-MULTIPLIER CIRCUIT

LETTER TO THE OFFICIAL DRAFTSPERSON

Assistant Commissioner for Patents  
ATTN: Official Draftsperson  
Washington, D.C. 20231

APPROVED BY  
THE EX *W*

Dear Sir:

MAILING CERTIFICATE UNDER 37 C.F.R. §1.8(A)  
I hereby certify that the above correspondence is being  
deposited with the U.S. Postal Service as First Class Mail  
in an envelope addressed to: Assistant Commissioner for  
Patents, Washington, D.C. 20231 on:

2-17-03

*Tommie Chambers*  
Tommie Chambers

Please find enclosed six (6) sheets of formal drawings as required by the  
Examiner.

Respectfully submitted,

*W. Daniel Swayze, Jr.*  
W. Daniel Swayze, Jr.  
Reg. No. 34,478  
Attorney for Applicants

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